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**BOX AF**

RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 1764

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Confirmation No.: 7046

KOMURA et al.

Group Art Unit: 2816

Application No.: 09/921,561

Examiner: Minh T. Nguyen

Filed: August 6, 2001

Attorney Dkt. No.: 024016-00012

For: DELAY CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
CONTAINING A DELAY CIRCUIT AND DELAY METHOD

**RESPONSE UNDER 37 C.F.R. § 1.116**

**BOX AF**

Director for the U.S. PTO  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: December 29, 2003

Sir:

This paper is filed in response to the Office Action dated September 26, 2003  
(December 26, 2003 – Patent Office Closed; December 27, 2003 – Saturday; and  
December 28, 2003 – Sunday). In the prosecution of the application identified above,  
please consider the following:

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**IN THE DRAWINGS:**

Please replace Fig. 6 with the attached amended Fig. 6.